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			Application Number	10/779,748	
			Filing Date	February 18, 2004	
			First Named Inventor	Ali E. Zadeh	
			Art Unit	2819	
			Examiner Name	J. J. Lauture	
Sheet	1	of	1	Attorney Docket Number	M4065.0967/P967

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
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FOREIGN PATENT DOCUMENTS						
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NON PATENT LITERATURE DOCUMENTS				
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JL	CA	"Understanding Pipelined ADCs", Maxim Integrated Products, www.maxim-ic.com/appnotes.cfm/appnote_number/383 , 2003.		
JL	CB	BRIAN BLACK, "Analog-to-Digital Converter Architectures and Choices for System Design", Analog Dialogue 33-8 (1999).		

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Examiner Signature		Date Considered	10/27/2004
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